



Huawei is a leading telecom solutions provider. Through continuous customer-centric innovation, Huawei has established end-to-end advantages in Telecom Network Infrastructure, Application & Software, Professional Services and Devices. With comprehensive strengths in wireline, wireless and IP technologies, Huawei has gained a leading position in the All-IP convergence age. Its products and solutions have been deployed in over 100 countries and have served 45 of the world's top 50 telecom operators, as well as one third of the world's population.

Huawei Leuven R&D team develops next generation RF transceivers, supporting 4G (LTE) and 5G protocols.

Today, we're seeking team players who get things done and share a passion for bringing new wireless technologies to the rapidly growing mobile market. Now is your chance to join the engineering team and develop new, world-leading products.

Job Description

We are offering a **SENIOR IC DESIGN ENGINEER** position enabling you to join the creation of the next generation mobile phone chips.

The ability to analytically reason on different circuit topologies, based on an overview of the state-of-the-art, select the right architecture and tweak the right parameters to meet stringent criteria for these circuits is your strength.

The candidate will design (IC design), simulate, layout and test integrated RF circuits in the 100 MHz to 15 GHz range in advanced CMOS or SOI technologies.

He/she will also perform experimental verification and debugging, along with the integration of complex RF & Mixed Signal functions into system chips. Building accurate models based on layout extraction and 2D/3D modeling tools to match measurements with simulations is essential.

Required Education and Experience:

- MSEE with several years industrial experience

Or relevant and excellent Ph.D., preferably complemented by a few years industry experience.
- Proven track record with silicon measured results
- Expertise in CMOS integrated (digitized or analog) Tx architectures, advanced Rx architectures, or the candidate should have several years of professional experience in analog/mixed-signal CMOS IC design.
- Product experience and expertise in any of these domains is a plus: **integrated PA, LNA's, VCO's, Nyquist DAC**
- Experience with Cadence Spectre, Virtuoso or other equivalent Agilent and/or Mentor tools is required.



- Good communication skills & team-player.
- Result driven
- Continuous strive for improvement in circuits and process.
- Detail oriented and determined
- Willing to relocate to Belgium, if applicable.

Location

Leuven, Belgium 100 % on site

How to apply

Send your resume to Werner.Seerden@huawei.com and wenjing.zhang1@huawei.com